

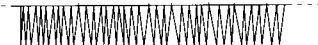

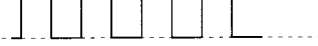
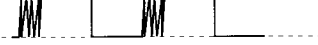
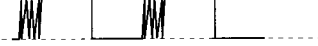




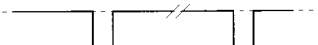






| Comp | Name (pin) Circ /IC | Scope | Freq. | Data H/L/A | Description |
|------|------------------------|---|-----------|---------------|--------------------------------------|
| 51 | ADV/ADV | Unstable  | 1.200 MHz | A | Address Valid Not |
| 52 | SLADV | 1 | 0 | H | Slow Address Valid Not (not used) |
| 53 | RDIN/RDIN | Unstable  | 1.200 MHz | A | Read In |
| 54 | WRIN/WRIN | Unstable  | 33 kHz | A | Write In |
| 55 | UPCLOCK/UPCLOCK |  | 6.248 MHz | A | Micro Processor Clock |
| 56 | ACQRDY/ACQRDY |  | 5Hz | A | Acquisition Ready |
| 57 | PS2/PS2 | Unstable  | 130 Hz | A | Page Select 2 |
| 58 | PS1/PS1 |  | 0 | O | Page Select 1 |
| 59 | PS0/PS0 | Unstable  | 125 Hz | A | Page Select 0 |
| 60 | .../OSCI |  | 25 MHz | X | Oscillator In |
| 61 | .../OSCO |  | 25 MHz | X | Oscillator Out |
| 62 | /HLDINN | Unstable  | 5 Hz | A | Hold In Not |
| 63 | /HLDOUTN | Unstable  | 5 Hz | A | Hold Out Not |
| 64 | ADC7/ADC7 | Unstable  | 100 Hz | A | ADC data output 7 |
| 65 | ADC6/ADC6 | Unstable  | 100 Hz | A | ADC data output 6 |
| 66 | ADC5/ADC5 | Unstable  | 100 Hz | A | ADC data output 5 |
| 67 | +5VD/VDD |  | 0 | H | Volt Supply |